Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) An image-processing apparatus, comprising:

n image processing sections which receive n consecutive pixel data items that are respectively input with the same timing and which respectively process the respective input pixel data items with the same timing, n representing an integer equal to at least "2"; and

a control section that controls the n image processing sections;

wherein each of the image processing sections are capable of being set to one of a first operation mode allowing data communication with the control section, and a second operation mode allowing only reception from the control section, one of the image processing sections is set to the first operation mode, and n−1 of the image processing sections are set to the second operation mode;

wherein commands are commonly given to the n image processing sections from the control section; and

wherein, when a command is given from the control section to the one of the image processing sections that is set to the first operation mode, the n image processing sections individually execute the same processing with the same timing.

2. (Previously Presented) The image-processing apparatus according to Claim 1, the n image processing sections being allocated in the same address space in address spaces that can be controlled by the control section.

3. (Previously Presented) The image-processing apparatus according to Claim 1, each of the image processing sections including a mode-setting terminal that sets one of the

first operation mode and the second operation mode, and one of the operation modes being set according to a mode-setting signal input to the mode-setting terminal.

4. (Previously Presented) The image-processing apparatus according to Claim 1, further including a memory that stores image-processing data commonly used by the respective image processing sections,

wherein the image processing section set to the first operation mode can write the image-processing data, which is fed from the control section, to the memory, and in addition, can read out the image-processing data written in the memory; and

wherein the image processing section set to the second operation mode can input the image-processing data read out by the image processing section set to the first operation mode from the memory.

5. (Previously Presented) An image-displaying apparatus, comprising:

the image-processing apparatus according to Claim 1, and

an image-displaying section that displays images represented by video signals output from the image-processing apparatus.

6. (Previously Presented) The image-processing apparatus according to Claim 2, each of the image processing sections including a mode-setting terminal that sets one of the first operation mode and the second operation mode, and one of the operation modes being set according to a mode-setting signal input to the mode-setting terminal.

7. (Previously Presented) The image-processing apparatus according to Claim 2, further including a memory that stores image-processing data commonly used by the respective image processing sections,

wherein the image processing section set to the first operation mode can write the image-processing data, which is fed from the control section, to the memory, and in addition, can read out the image-processing data written in the memory; and

wherein the image processing section set to the second operation mode can input the image-processing data read out by the image processing section set to the first operation mode from the memory.

8. (Previously Presented) The image-processing apparatus according to Claim 3, further including a memory that stores image-processing data commonly used by the respective image processing sections,

wherein the image processing section set to the first operation mode can write the image-processing data, which is fed from the control section, to the memory, and in addition, can read out the image-processing data written in the memory; and

wherein the image processing section set to the second operation mode can input the image-processing data read out by the image processing section set to the first operation mode from the memory.

9. (Previously Presented) An image-displaying apparatus, comprising:

the image-processing apparatus according to Claim 2, and

an image displaying section that displays images represented by video signals output from the image-processing apparatus.

10. (Previously Presented) An image-displaying apparatus, comprising:

the image-processing apparatus according to Claim 3, and

an image displaying section that displays images represented by video signals output from the image-processing apparatus.

11. (Previously Presented) An image-displaying apparatus, comprising:

the image-processing apparatus according to Claim 4, and

an image displaying section that displays images represented by video signals output from the image-processing apparatus.

12. (Previously Presented) An image-processing apparatus according to claim 1, further comprising:

a CPU data bus,

the image processing section that is set to the first operation mode is permitted input access and/or output access to the CPU data bus, and the image processing sections that are set to the second operation mode are inhibited from outputting data to the CPU data bus.

13. (Previously Presented) An image-processing apparatus according to claim 1, each of the image processing sections including a mode control section that sets to one of the first operations mode and the second operation mode.

14. (New) The image-processing apparatus according to claim 1, further comprising:

a scan converter that outputs synchronization signals as a basis for the same timing.